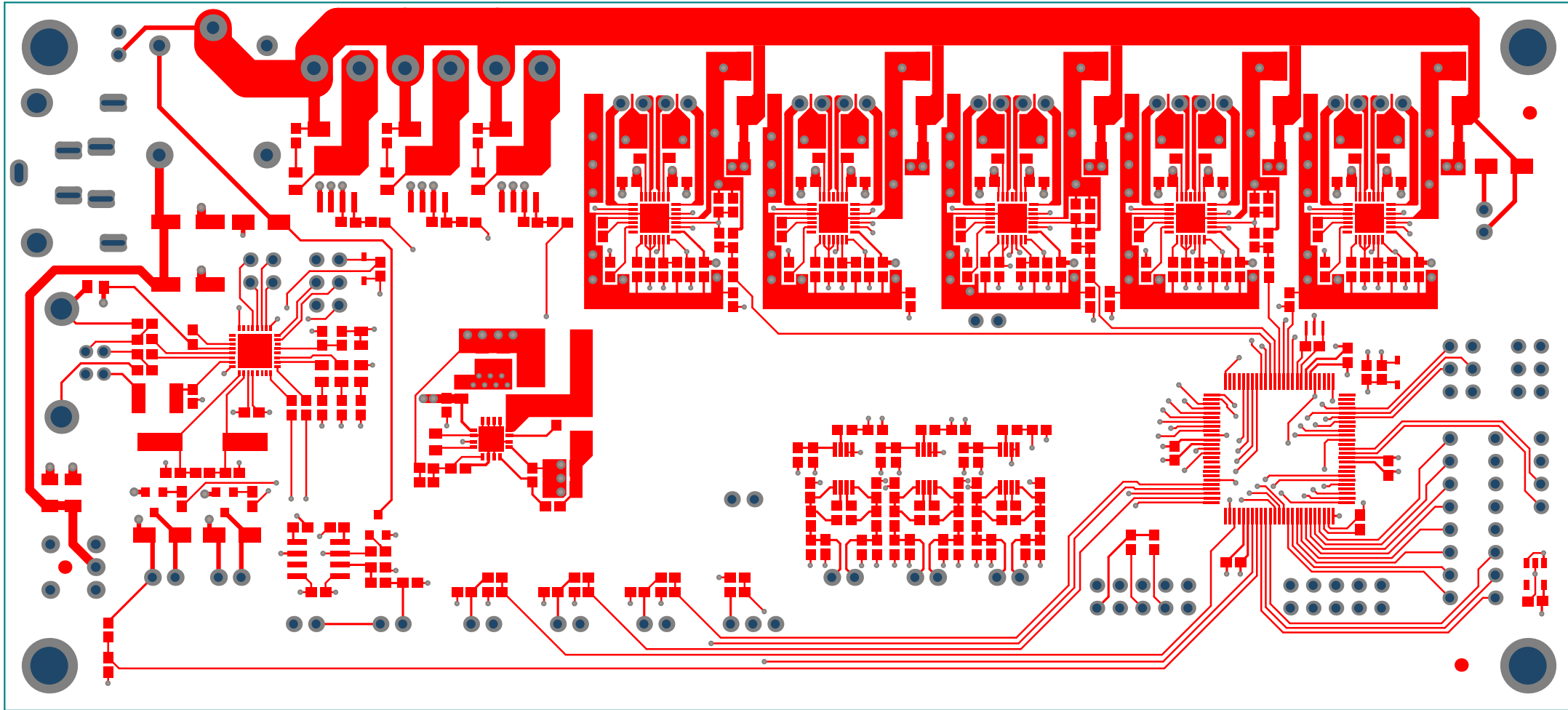
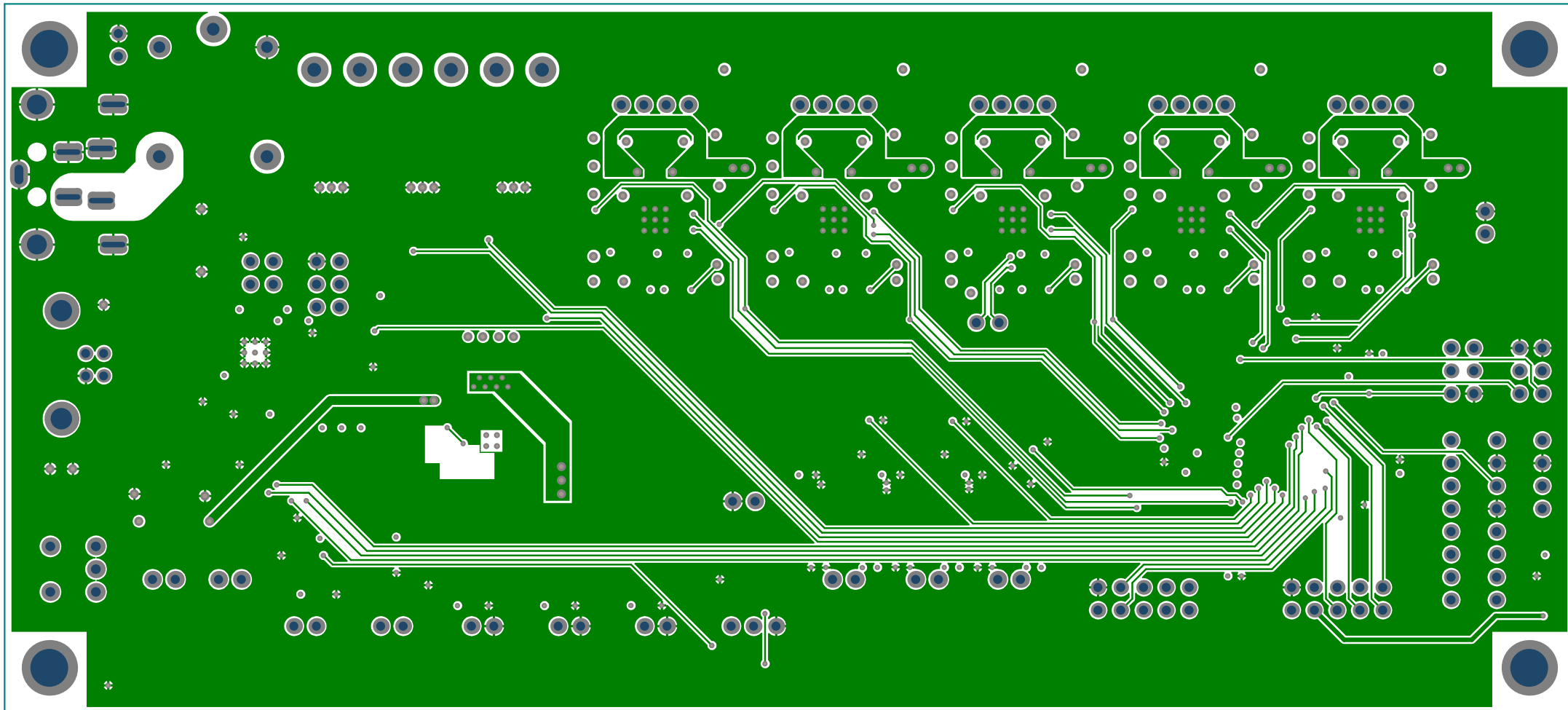


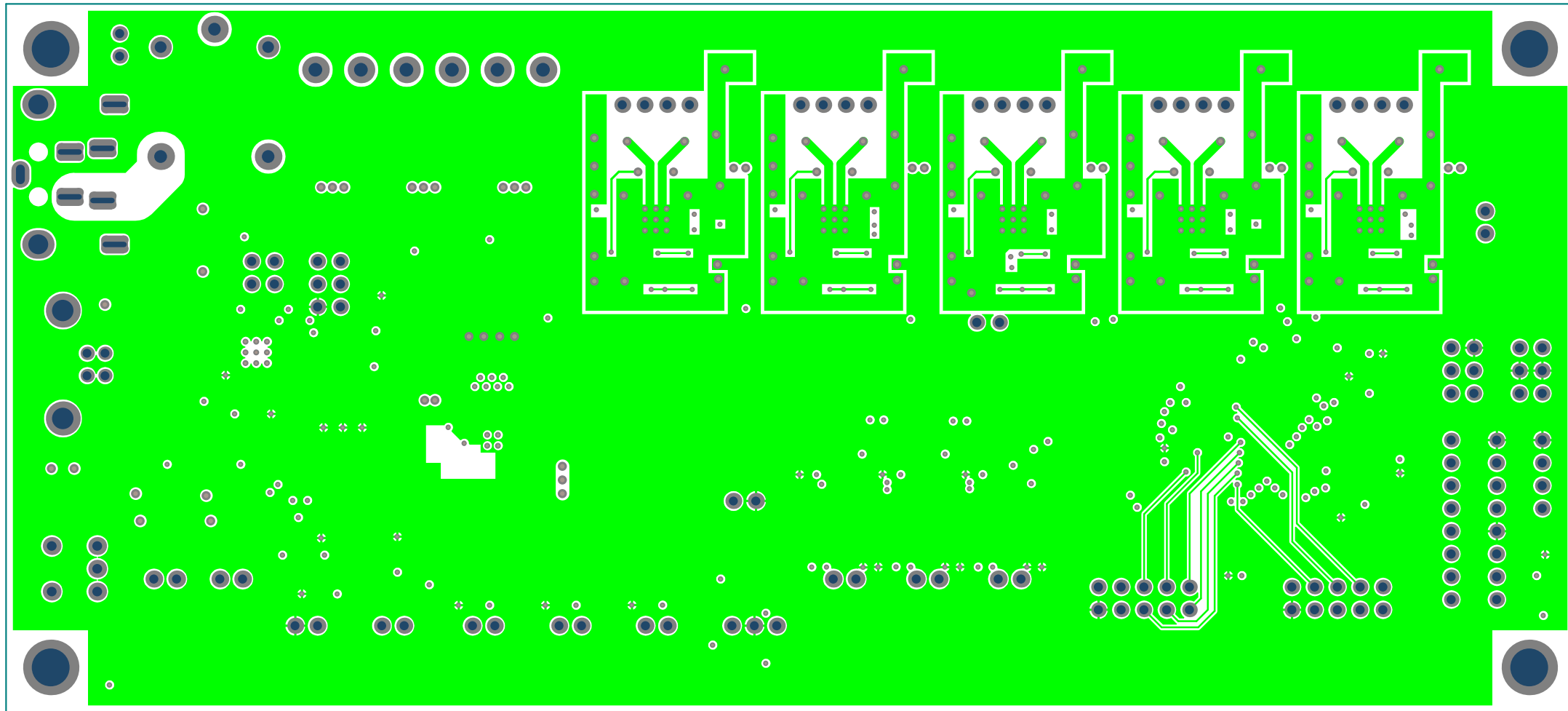
# Copper top 70u



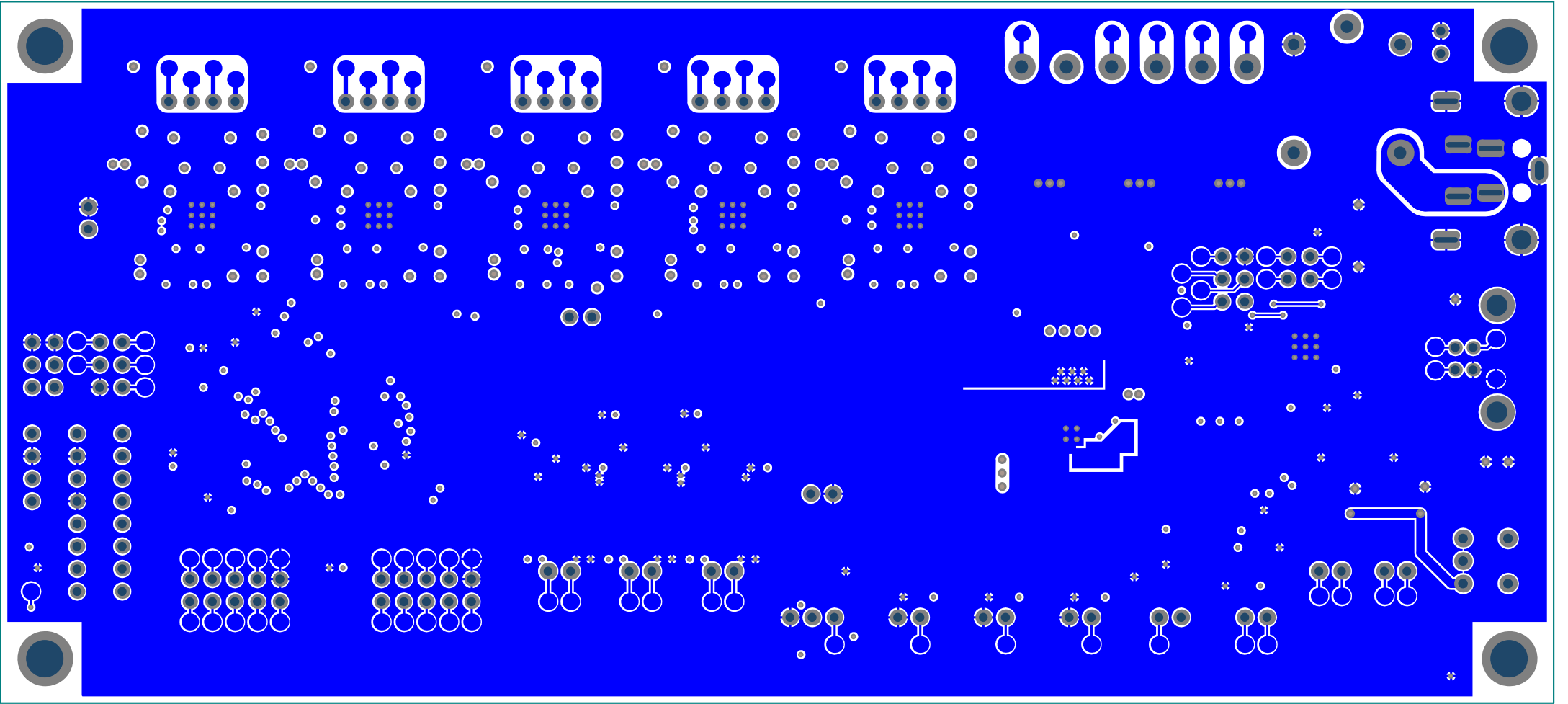
# Copper inner1 35u



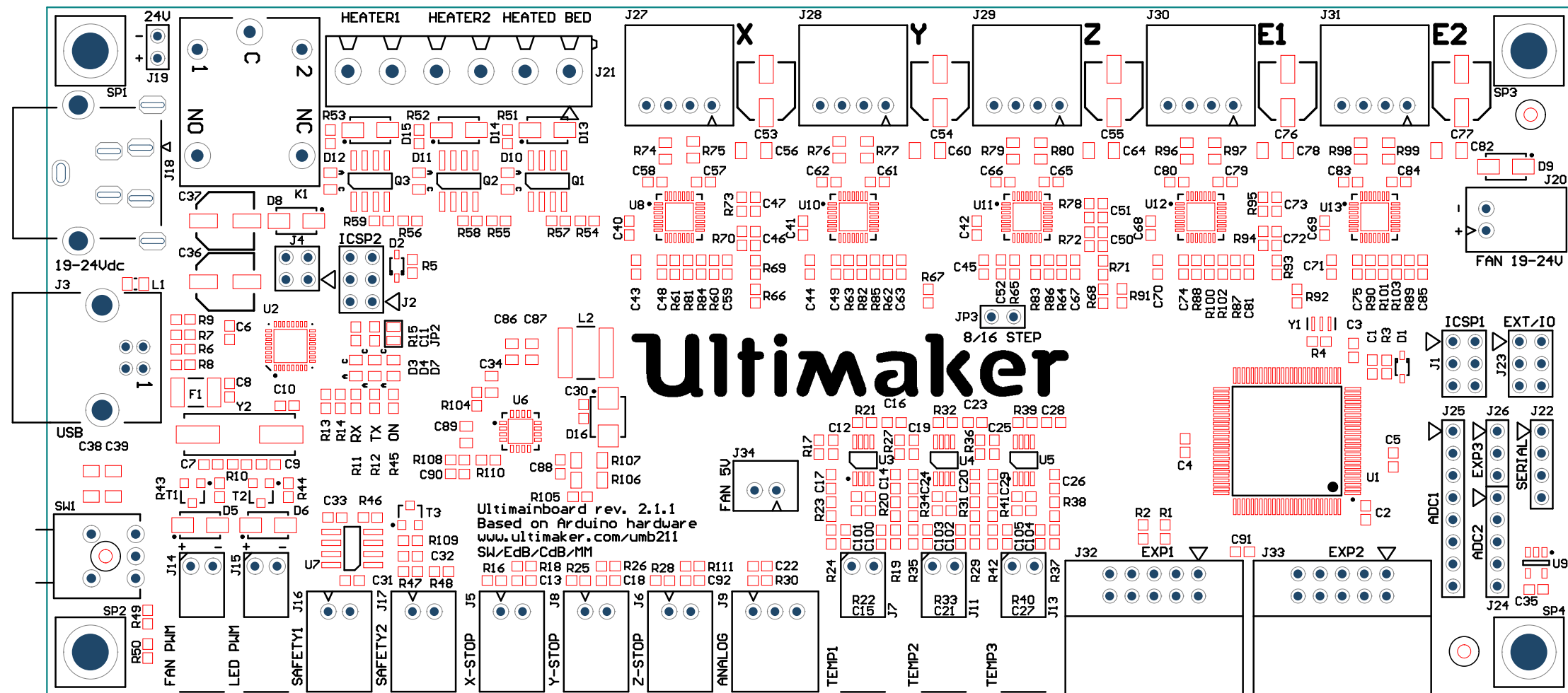
Copper inner2 35u

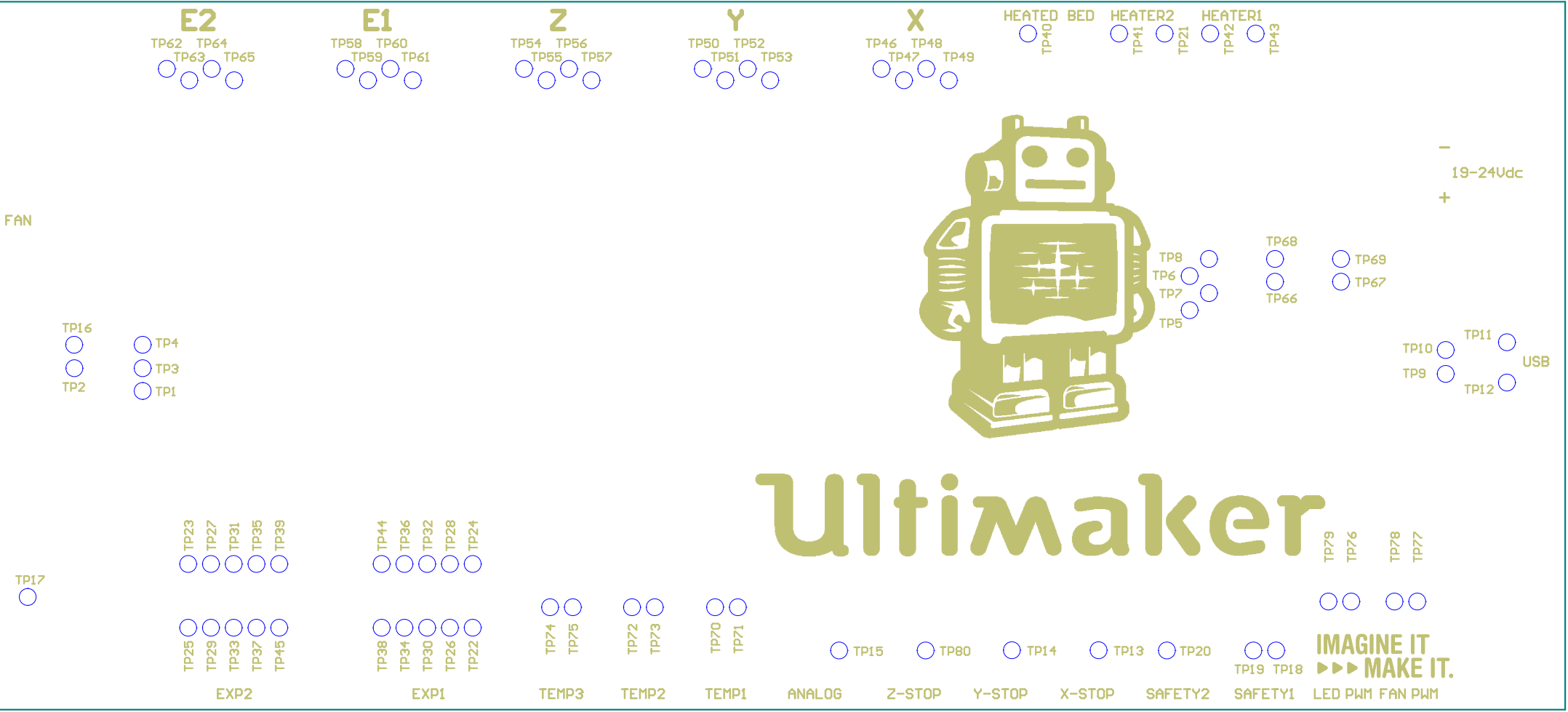


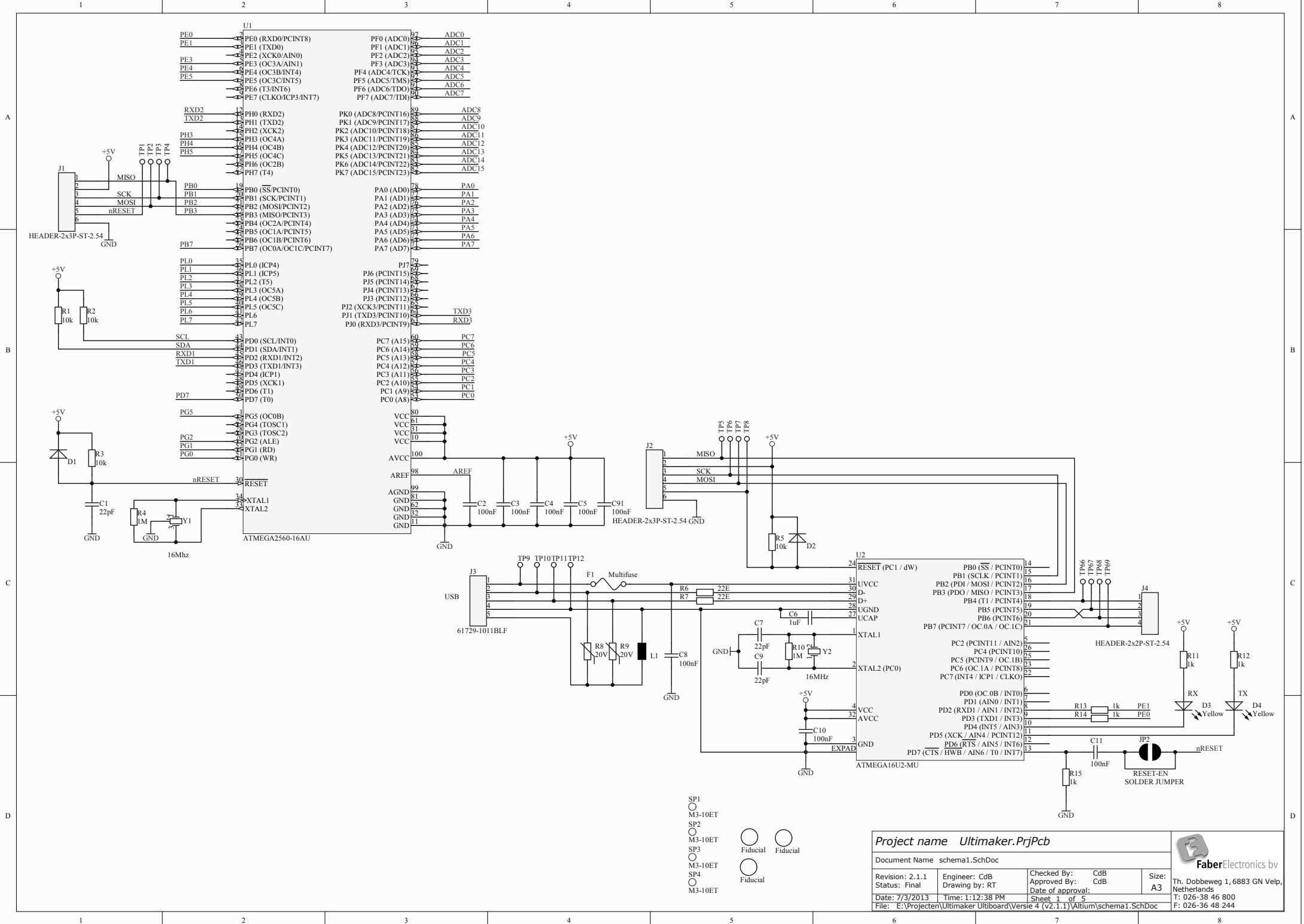
# Copper pot 50u

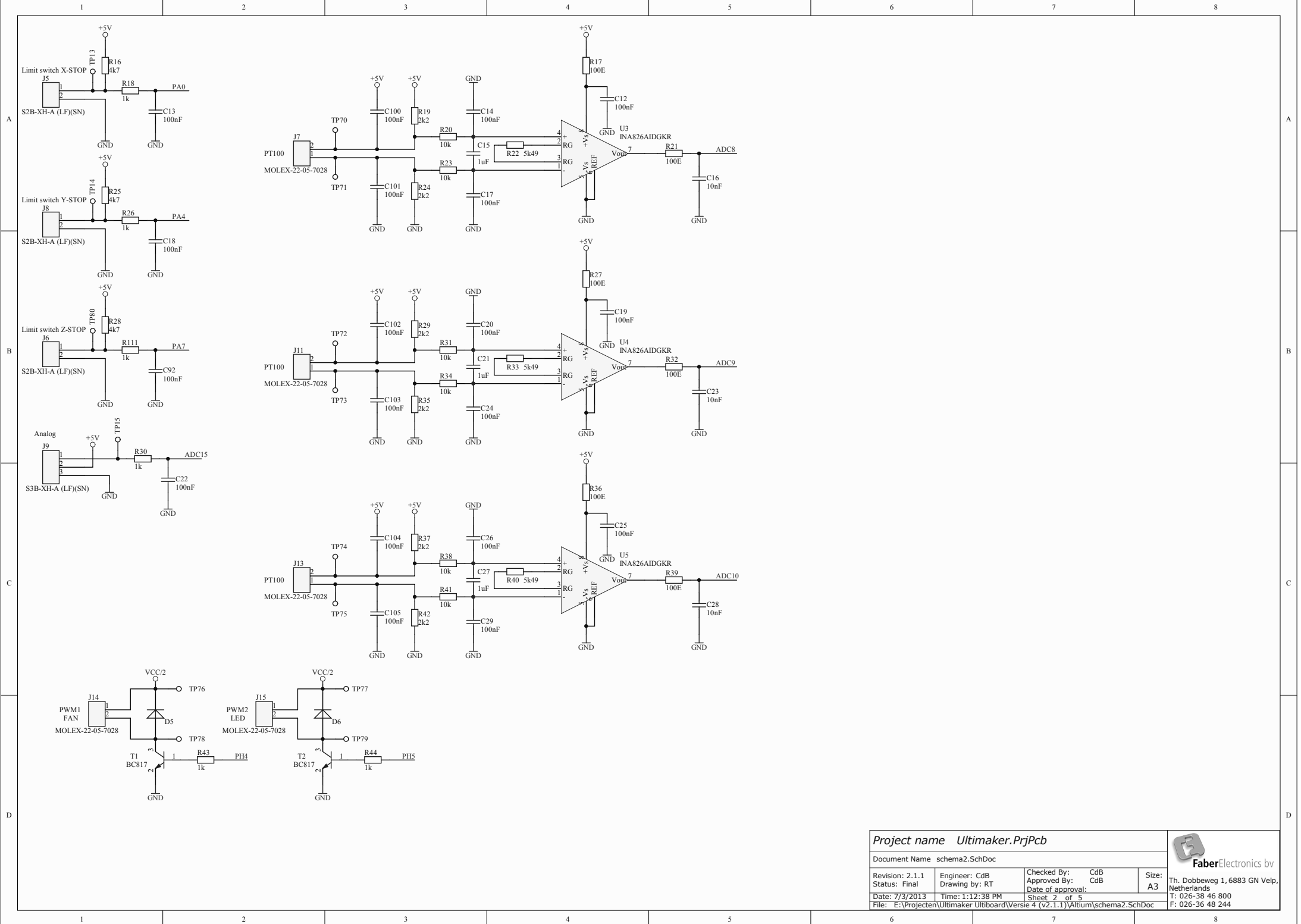












Project name *Ultimaker.PrjPcb*

Document Name *schema2.SchDoc*

Revision: 2.1.1

Status: Final

Date: 7/3/2013

File: E:\Projecten\Ultimaker\Ultiboard\Versie 4 (v2.1.1)\Altium\schema2.SchDoc

Engineer: CdB

Drawing by: RT

Time: 1:12:38 PM

Sheet 2 of 5

Checked By: CdB

Approved By: CdB

Date of approval:

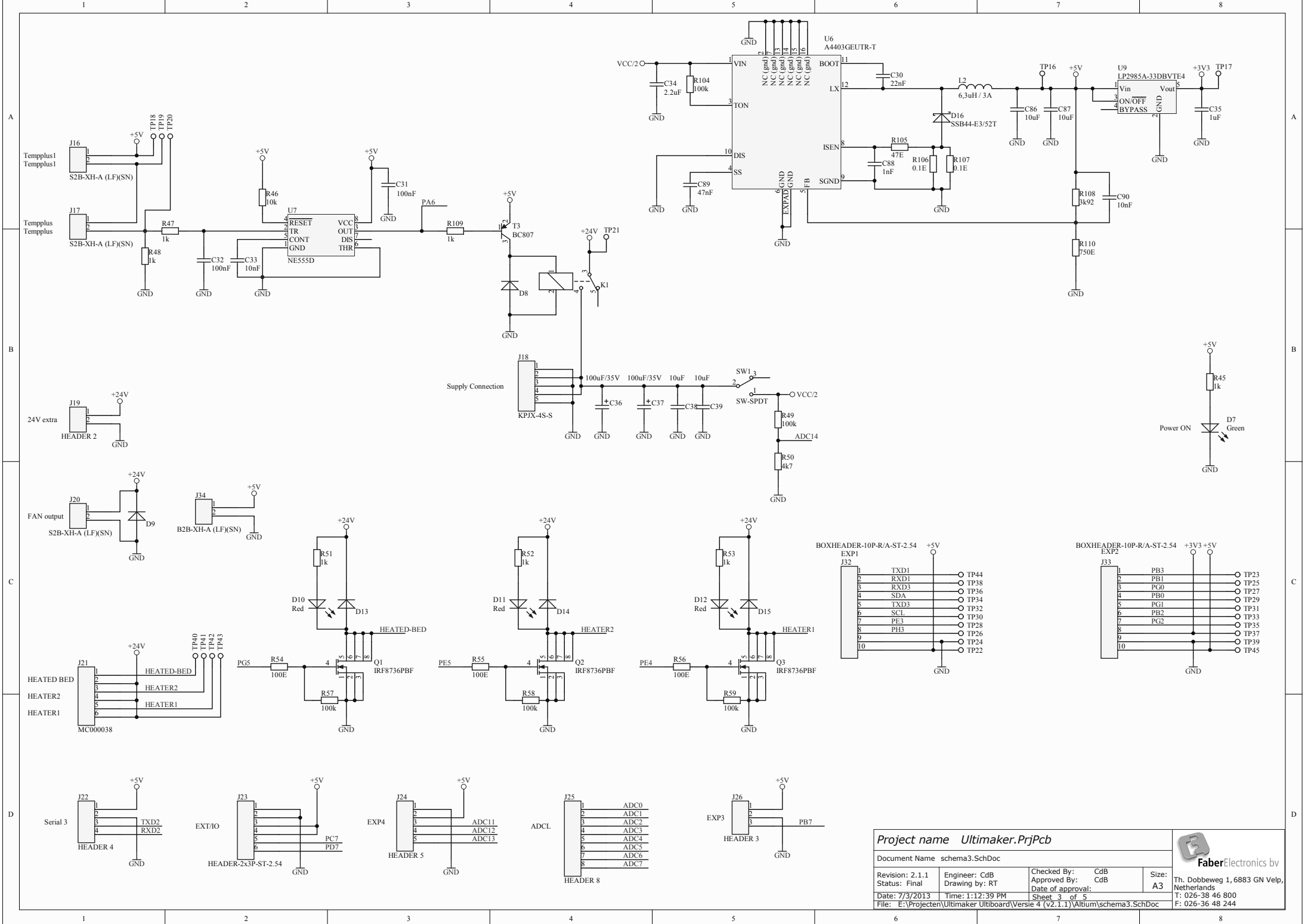
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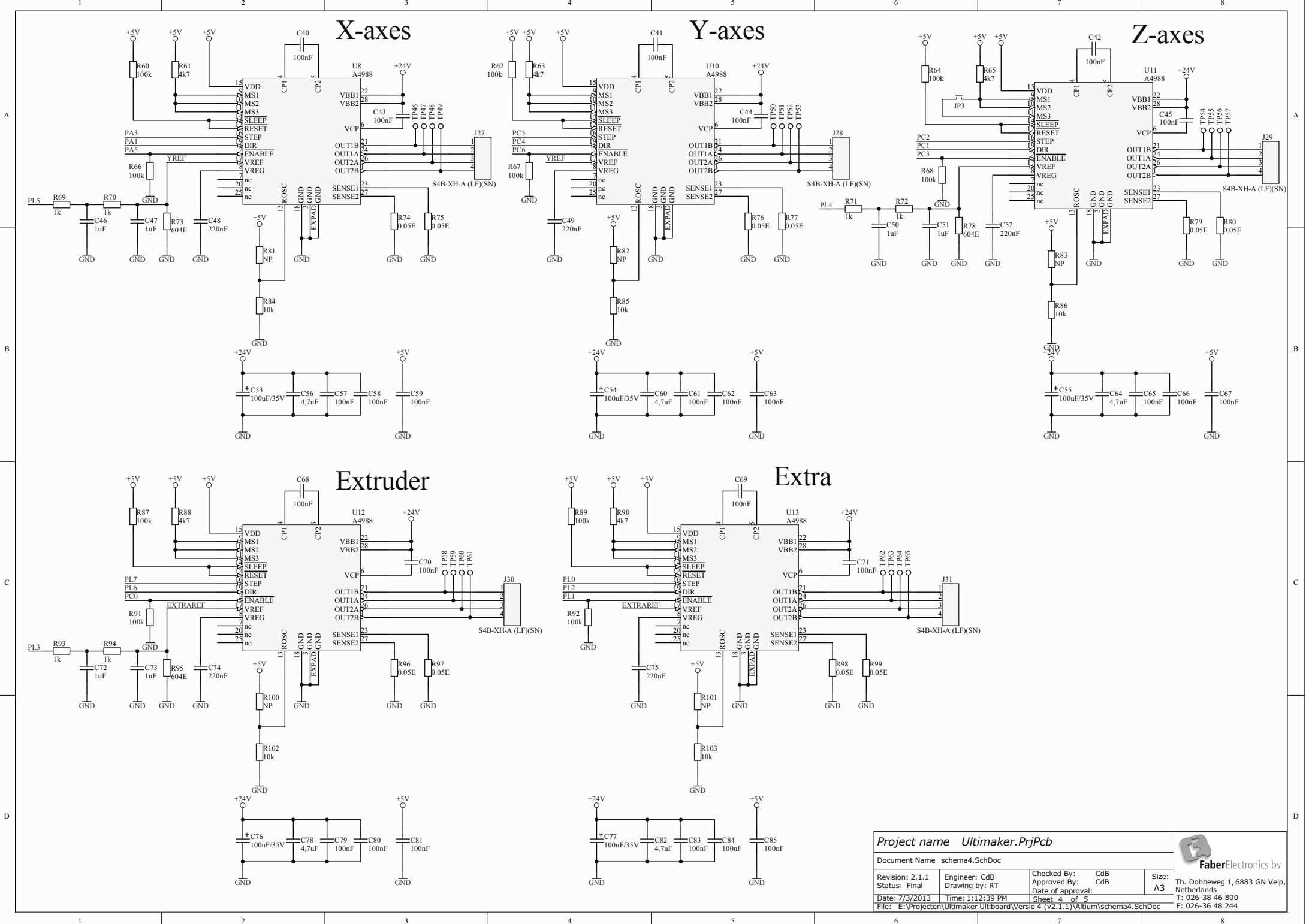
Size:


A3




Faber Electronics bv  
Th. Dobbeweg 1, 6883 GN Velp,  
Netherlands  
T: 026-38 46 800  
F: 026-36 48 244

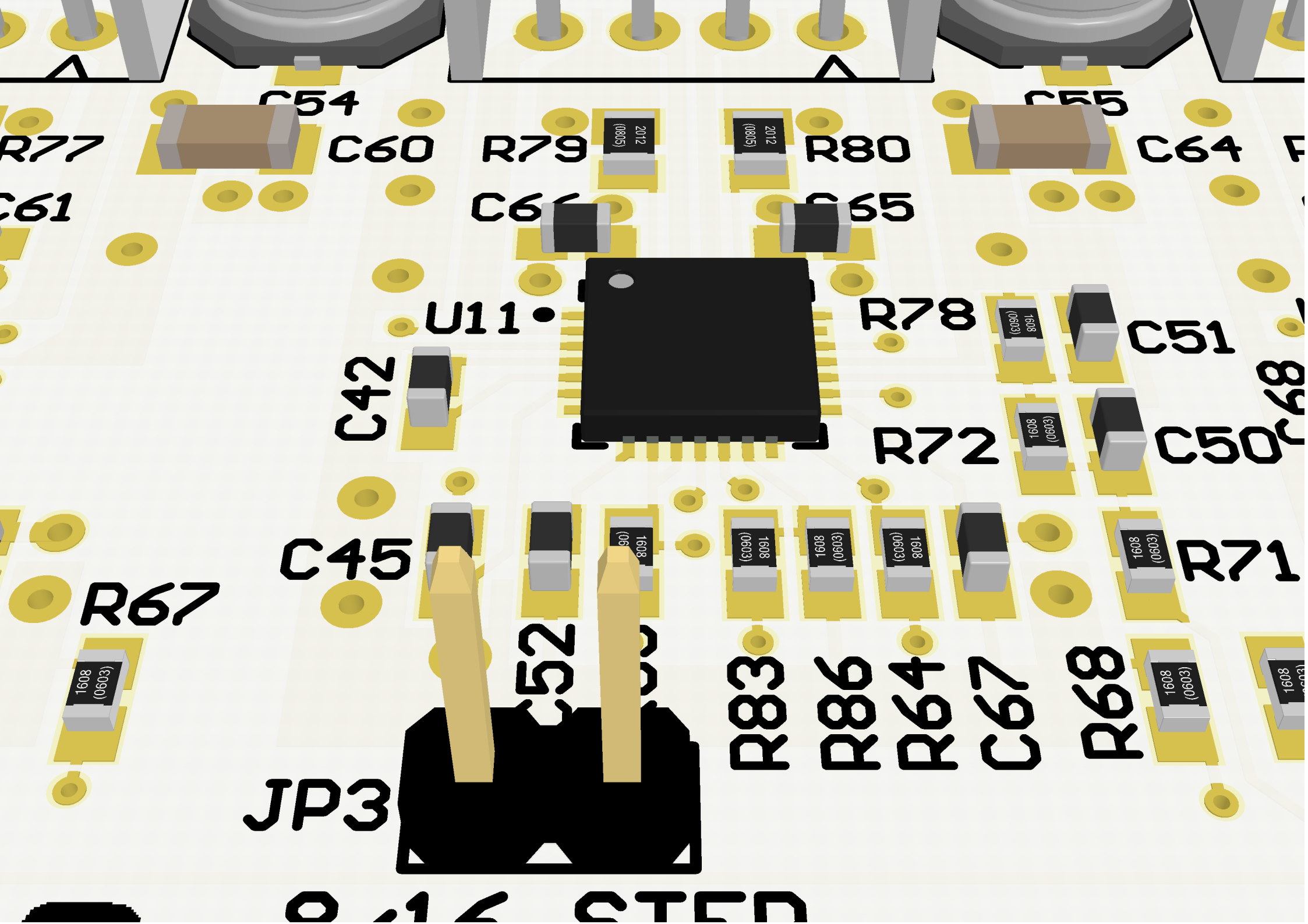




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Revision: 2.1.1	Engineer: CdB	Checked By: CdB	Size: A3	Th. Dobbeweg 1,6883 GN Velp, Netherlands T: 026-38 46 800 F: 026-36 48 244	
Status: Final	Drawing by: RT	Approved By: CdB			
Date: 7/3/2013	Time: 1:12:39 PM	Date of approval:	Sheet 4 of 5		
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C								
D								
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Project name <i>Ultimaker.PrjPcb</i>				 <b>Faber</b> Electronics bv
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Revision: 2.1.1 Status: Final	Engineer: CdB Drawing by: RT	Checked By: CdB Approved By: CdB Date of approval:	Size: A3	Th. Dobbeweg 1, 6883 GN Velp, Netherlands T: 026-38 46 800 F: 026-36 48 244
Date: 7/3/2013	Time: 1:12:39 PM	Sheet 5 of 5		
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C54

C55

R77

C60

R79

2012  
(0805)

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R80

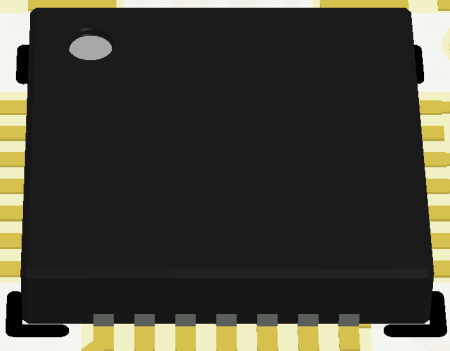
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R78

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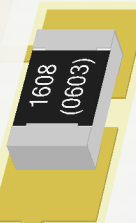
R72

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C50

C45

R67



C52

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1608  
(0603)

R71

JP3



R83

R86

R64

C67

R68

1608  
(0603)

1608  
(0603)

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